CMPT 295

Unit - Machine-Level Programming

Lecture 13 – Assembly language – Program Control – **cmovX** Iterative Statements – Loops

Last Lecture

- In C, we can change the execution flow of a program
 - 1. Conditionaly
 - Conditional statements: if/else, switch
 - Iterative statements: loops
 - 2. Unconditionally
 - Functions calls
- In x86-64 assembly, we can also change the execution flow of a program
 - cmp* instruction (compare)
 - j X instructions (jump)
 - call and ret instructions

Today's Menu

Introduction

- C program -> assembly code -> machine level code
- Assembly language basics: data, move operation
 - Memory addressing modes
- Operation leag and Arithmetic & logical operations
- Conditional Statement Condition Code + cmovX
- Loops
- Function call Stack
- Array
- Buffer Overflow
- Floating-point operations

Homework: int max(int x, int y)

version 1 – with **jx** instruction

```
In C:
int max(int x, int y) {
  int result = x;
  if (y > x)
   result = y;
  return result;
}

In Assembly: # x in %edi, y in %esi, result in %eax
max:
  movl %edi, %eax # result = x
  cmpl %edi, %esi # if y <= x then
  jle endif # return 
  movl %esi, %eax # result = y (if y) x)
endif:
  ret
```

We branch (jump) when the condition (y > x) is false, i.e., when (y <= x) -> This technique is called "coding the false condition first" or "taking care of ..."

Conditional move instruction **cmovX**

What C code looks li	ke What logic of assembly
when using condition	nal code looks like when using
operator:	cmovX (expressed in C):
result = test ? va	12 : val1; result = val1;
return result;	if (test) result = val2;
	return result;
Example: cmovle	Src, Dest alternative: int abs(int x)
in C:	in assembly: # x in %edi, result in %eax
<pre>int abs(int x) {</pre>	abs:
if $(x < 0)$	movl %edi, %eax # result = x 4
$\mathbf{x} = -\mathbf{x};$	negl %edi # x = -x
return x:	cmpl \$0, \$eax # if x < 0 then <
}	<pre>cmovl %edi, %eax # result = -x </pre>
,	V ICU

Advantage of conditional move **cmovX**

Note about **branching**:

- Branches are very disruptive to instruction flow through microprocessor (CPU) pipelines
- However, since conditional moves (cmovX) do not require control transfer (no branching/jumping required), they are less disruptive
- So, gcc tries to use them, but only when safe

What do we mean by "safe"?

In result = test ? aVal : anotherVal; both values

(aVal and anotherVal) are computed so their computation must be "safe"

- Example of unsafe computations:
 - 1. Expensive computations val = Test(x) ? Hard1(x) : Hard2(x);

Only makes sense when computations are very simple

- 2. Risky computations **val** = **p** ? ***p** : **0**;
 - Only makes sense when computations do not crash the application
- 3. Computations with side effects val = x > 0? x*=7: x+=3;
 - Only makes sense when computations do not have side effects

Homework: Example: alternate int max(int x, int y)

In C: int max(int x, int y) { int result = x; if (y > x) result = y; return result;

```
version 2 - with cmovx instruction
In Assembly: # x in %edi, y in %esi, result in %eax
max:
    movl %edi, %eax # result = x
    cmpl %edi, %esi # if y > x then
    cmovg %esi, %eax # result = y
    ret
```



While loop – "jump-to-middle"

int x and int y are arguments to function



Here, we jump back into the loop when condition is true!

Do While loop - "jump-to-middle"

int x and int y are arguments to function



1) Here, are we "cooling the false condition first ? For loop (2) which loop pattern are we using? In Assembly: # n in %edi, i in %ecx In C: initialization
for (i = 0; i < n; i++) {</pre> xorl %ecx, %ecx # initialization // stmts condition
 testing # %ecx (i) <- 0 loop: cmpl %edi, %ecx # while i < n true</pre> testing # return; # jump when i >= n jge endloop¥ false condition # i = 0; // initialization while (i < n) { //condition # stmts testing // stmts incl %ecx # i++ increment i++; // increment # loop again jmp loop endloop: return; ret * In this situation "je" would also work. Do you see why?

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Compiler can produce different instruction combinations when assembling the same C code.

Summary

- In x86-64 assembly, there are no conditional statements, however, we can alter the execution flow of a program by using ...
 - cmp* instruction (compare)
 - jx instructions (jump)
 - call and ret instructions
 - cmovx instructions -> conditional move
- In x86-64 assembly, there are no iterative statements, however, we can alter the execution flow of a program by using ...
 - cmp* instruction
 - ► jx instructions (jump)

cmp* and test*
instructions set
condition codes

- CPU uses these condition codes to decide whether a ...
 - jx instruction (conditional jump) is to be exectued or a
 - cmovx instruction (conditional move) is to be exectued
- 2 loop patterns:
 - "coding the false condition first" -> while loops (hence for loops)
 - "jump-in-middle" -> while, do-while (hence for loops)

Next Lecture

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- Operation leag and Arithmetic & logical operations
- Conditional Statement Condition Code + cmovX
- Loops
- Function call Stack
 - Overview of Function Call
 - Memory Layout and Stack x86-64 instructions and registers
 - Passing control
 - Passing data Calling Conventions
 - Managing local data
- Array
- Buffer Overflow
- Floating-point operations